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Rev 7-80U.S. Dept of Commerce
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99-TK-565SSSerial No.
09/410,646Applicant
David Edwards et al.LIST OF PRIOR ART CITED BY APPLICANT
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Filing Date
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS/ SUBCLASS	FILING DATE
MM AA	5,386,565	01/31/95	Tanaka et al.	395/700	01/11/91
AB	5,423,050	06/06/95	Taylor et al.	395/575	09/23/94
AC	5,434,804	07/18/95	Bock et al.	364/579	12/29/93
AD	5,440,705	08/19/95 04/30/90	Wang et al.	395/421.1	04/30/90
AE	5,448,576	09/19/95 10/29/92	Russell	371/22.3	09/05/95
AF	5,452,432	09/19/95	Macachor	395/425	07/23/93
AG	5,455,936	10/03/95	Maemura	395/183.11	04/28/94
AH	5,483,518	01/09/96	Whetsel	370/13	04/24/95
AI	5,488,688	01/30/96	Gonzales et al.	395/183.1	03/30/94
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AK	5,590,354	12/31/96	Klapproth et al.	395/800	07/28/94

FOREIGN COUNTRIES

	DOC. NO.	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION
MM AL	EP0165600B1	11/21/91	Europe	G06F 13/36	English
MM AM	EP0636976A1	02/01/95	Europe	G06F 11/00	English

OTHER PRIOR ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGE, ETC.)

MM AN Richard York; Real Time Debug for System-on-Chip Devices; June 1999; Pages 1-6

EXAMINER Michael Moshulinski DATE CONSIDERED 4/30/02

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with PEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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U.S. PATENT DOCUMENTS

FOREIGN COUNTRIES

DOC. NO.	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION	
MM CU	PCT/JP96/02819	09/27/96 Japan	W0X	G06F-009/46	Japanese
MM CV	EP0652516A1	5/10/95 Europe		G06F-11/00	English

OTHER PRIOR ART (INCLUDING AUTHOR, TITLE, DATE, PERTINENT PAGE, ETC.)

EXAMINER Michael Maslenikov DATE CONSIDERED 4/30/02

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